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09/626146

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257 710

Class Subclass

ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED PD² O.I.P.E. PATENT DATE

APPLICATION NO. 09/626146

CLASS 438

SUBCLASS 710

ART UNIT 2812

EXAMINER B. Nadav

TOSHINORI NAKAYAMA

TITLE

SUBSTRATE FOR SEMICONDUCTOR DEVICE, SEMICONDUCTOR CHIP MOUNTING SUBSTRATE, SEMICONDUCTOR DEVICE AND METHOD OF FABRICATION THEREOF, AND CIRCUIT BOARD, TOGETHER WITH ELECTRONIC EQUIPMENT

PTO-2040 12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
257	710	257	704	736	788		
INTERNATIONAL CLASSIFICATION							
H 01 L	23/10						
H 01 L	23/12						

☐ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	8	16	13		1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	OKI NADAV 11/16/02 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	TOM THOMAS SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800 T. Thomas 11-7-02 (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	

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